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IMAGE DISPLAY SCREEN AND METHOD OF ADDRESSING SAID
SCREENABSTRACT

The invention concerns a display screen including:

- light emitters (4) distributed in rows of light emitters and columns of light emitters,
- a first addressing circuit (6, 14, 16, 18), associated with each light emitter of the array, said circuit (6) including a first current modulator (14) intended to power said light emitter (4) and a first storage capacitor (16) intended to store a potential at the gate electrode of the first current modulator (14).

The screen includes at least a second addressing circuit (12, 34, 36, 38) for a light emitter, the first and the second addressing circuits being associated with the same light emitter, the second circuit including a second current modulator (34) for said light emitter and a second storage capacitor (36) intended to store a potential at the gate electrode of the second current modulator.

The invention also concerns an addressing method for this screen.

Figure 1.